

# MONITOR HAVING A SELF TESTING CIRCUIT

## BACKGROUND OF THE INVENTION

### 5 1. Field of the Invention

The present invention relates to a monitor. More specifically, the present invention relates to a monitor having a self testing circuit.

### 10 2. Description of the Prior Art

Prior art monitor utilizes its OSD (On Screen Display) function and a micro-controller to achieve self testing of the monitor. When a user wants to know if the monitor is in a good operation, he can disconnect the monitor from a computer.

15 If the monitor indicates a message, e.g. "no signal" on the panel successfully to show that no video signals from the computer are received, that means the monitor is not out of function.

20 When the monitor is disconnected from the computer, the micro-controller of the monitor is not able to receive video signals from the computer. Therefore, the micro-controller activates the OSD function of the monitor to indicate a message, e.g. "no signal" on the panel. If the message is successfully  
25 shown on the panel, it means the monitor is not out of function. Otherwise, it means the monitor is out of function.

However, not all monitors are accommodated with a micro-controller or OSD function. Such monitors are not  
30 capable of performing self testing function. That is, for those monitors that don't possess micro-controller and OSD function, they are, in general, not able to perform self testing

function.

## SUMMARY OF THE INVENTION

5        It is therefore a primary objective of this invention to provide a monitor having a self testing circuit. Self testing function can be achieved even lack of OSD facility and micro-controller in such a monitor.

10        The monitor according to the present invention comprises a display panel, a displaying circuit, a connector and a self testing circuit. The self testing circuit is electrically connected to an input port of the displaying circuit. It comprises a testing signal, a switch circuit and a detecting  
15        circuit. When the detecting circuit detects video signals transmitted from the computer, the detecting circuit switches off the switch circuit so as to avoid the testing signal generated from the testing signal generator being transmitted to the input port of the displaying circuit. When the detecting  
20        circuit detects no video signals from the computer, the detecting circuit switches on the switch circuit so as to allow the testing signal generated from the testing signal generator to be transmitted to the displaying circuit.

25        It is an advantage of the present invention that the monitor can perform self-testing without using OSD or the micro-controller installed therein.

30        These and other objects and the advantages of the present invention will no doubt become obvious to those of ordinary skill in the art after having read the following detailed description of the preferred embodiment that is illustrated

in the various figures and drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

5        Fig.1 is the functional block diagram of the monitor according to the present invention.

      Fig.2 is a perspective view of the connector and its pin definition.

10       Fig.3 is a perspective view of the self testing circuit in Fig.1.

      Fig. 4 is a perspective view of the H-BLANK signal and the full-white video image.

15       Fig. 5 is a flow chart of simulating video signals to generate video images with the H-BLANK signal according to the present invention.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

20       Please refer to Fig.1. Fig.1 is the functional block diagram of the monitor 10 according to the present invention. The monitor 10 comprises a display panel 12 for displaying video images, a displaying circuit 14 for transforming video signals from a computer 16 sequentially into corresponding scanning lines so as to form a corresponding video image on  
25       the display panel 12, a self testing circuit 20 electrically connected to an input port of the displaying circuit 14 for performing self testing function, and a connector 18 electrically connected to an input port of the self testing circuit 20 for receiving the video signals transmitted from  
30       the computer 16. When the displaying circuit 14 prepares to display each scanning line on the panel, it further depends on an H-BLANK signal to decide when to show this scanning line

on the panel 12, so that a plurality of scanning lines can form a corresponding video image.

Please refer to Fig.2. Fig.2 is a perspective view of the connector 18 and its pin definition. The connector 18 of the present invention can be a sub-D15 connector, in which the definition of fifteen pins thereon are shown in Fig. 2. The video signals transmitted from the computer 16 are IBM VGA signals, which including an EPS1 sub signal. There is also a corresponding EPS1 pin on the connector 18 for receiving the EPS1 sub signal from the computer 16, which is shown as the fifth pin in Fig. 2.

Please refer to Fig.3. Fig.3 is a perspective view of the self testing circuit 20 in Fig.1. The self testing circuit 20 includes a testing signal generator 22 for generating a testing signal to test the monitor 10, a switch circuit 24 electrically connected between an output port of the testing signal generator 22 and the input port of the displaying circuit 14, for controlling output of the testing signal, and a detecting circuit 26 electrically connected to a controlling port of the switch circuit 24 for detecting whether signals transmitted from the computer 16 exist or not, for controlling on/off states of the switch circuit 24.

When the detecting circuit 26 detects the video signals (IBM VGA signals) transmitted from the computer 16, the detecting circuit 26 switches off the switch circuit 24. Consequently, the testing signal generated from the testing signal generator 22 is not able to be transmitted to the input port of the displaying circuit 14 via the switch circuit 24. The connector 18 then input the video signals from the computer

16 into the displaying circuit 14. The displaying circuit 14 would then transform the video signals into a video image and display the video image on the panel 12.

5 When the detecting circuit 26 detects no video signals transmitted from the computer 16, the detecting circuit 26 switches on the switch circuit 24. The testing signal generated from the testing signal generator 22 is then allowed to transmit to the displaying circuit 14 via the switch circuit  
10 24. The displaying circuit 14 would then transform the testing signal into a testing image and display the testing image on the panel 12. For example, when the testing image is a full-white video image, then it means that the monitor is not out of function. Otherwise, it means that the monitor is out  
15 of function.

One of the objectives of the present invention is to allow the self testing function of a monitor which is even not accommodated with the OSD facility or micro-controller. For  
20 example, when the monitor passes the self testing, a full-white picture is shown on the panel so as to indicate a user or operator that the monitor is in its good operation. That is, the present invention needs a testing signal, which can substantially demonstrate a full-white picture, just as a  
25 full-white video signal can do. However, when processing the self test function, the monitor 10 is disconnected with the computer 16. Such a testing signal, which is capable of demonstrating a full-white picture, would not come from the computer 16, but is generated from the monitor 10 inside. One  
30 of the controlling signals, H-BLANK signal, for controlling the displaying circuit 14 in the monitor 10, happens to resemble a full-white video signal and can substantially show

a full-white picture. It is therefore convenient to utilize the H-BLANK signal as the necessary testing signal in the present invention.

5 Please refer to Fig. 4. Fig. 4 is a perspective view of the H-BLANK signal and the full-white video image. The H-BLANK signal 23 is a control signal for indicating when to display the video image during the horizontal scanning period in the monitor 10. The peak-to-peak amplitude of the H-BLANK  
10 signal 23 is about 5 volts, and its duty cycle is about 83%~90%. The peak-to-peak amplitude of a full-white video signal is about 0.7 volts, and its duty cycle is about 75%~80%. As shown in Fig.4, the waveform of the H-BLANK signal 23 is substantially similar to the waveform of a full-white video  
15 signal. The operation of the self testing circuit 20 is not influenced by the difference in the duty cycle. And the difference in the amplitude can be tuned by a simple resistance circuit to adjust the amplitude of the H-BLANK signal 23. Therefore, the H-BLANK signal 23 is available for the required  
20 testing signal in the present invention. If the H-BLANK signal 23 is utilized, a full-white picture would be displayed on the panel 12 when the monitor 10 passes the self testing. That is, the H-BLANK signal 23 can be simulated to generate the video signals, such as R1、G1、B1、CLAMP. They can then be  
25 transmitted to the displaying circuit 14 in combination or separately. If the monitor 10 is in a good operation, when the simulated R1、G1、B1 or CLAMP signal generated from the testing signal is transmitted to the displaying circuit 14 separately, the display panel 12 would generate corresponding  
30 R、G、B video image. When the simulated R1、G1、B1、CLAMP signal are transmitted to the displaying circuit 14 in combination, the display panel 12 would generate a full-white picture. The

latter is utilized in the preferred embodiment as a illustration, however, not as a limitation.

As shown in Fig. 3, the detecting circuit of the self  
5 testing circuit 20 comprises a first transistor 28 and a second  
transistor 30. The switch circuit 24 comprises a third  
transistor 32 to form a controlling port. When the connector  
18 is connected with the computer 16, the EPS1 pin 25 of the  
connector 18 is grounded. The EPS1 pin 25 is connected to the  
10 base of the first transistor 28. When the EPS1 pin 25 is  
grounded, the base of the first transistor 28 is in low voltage  
and the first transistor 28 is conducted. The first transistor  
28 is further connected to the base of the second transistor  
30. When the first transistor 28 is conducted, the base of  
15 the second transistor 30 is in high voltage and the second  
transistor 30 is therefore open, which results in the open  
state of the third transistor 32 in the switch circuit 24.  
The current from the power 29 is blocked by the third transistor  
32, and consequently the testing signal generated by the  
20 testing signal generator 22 can not be transmitted to the  
displaying circuit 14 via the switch circuit 24.

When the connector 18 is disconnected with the computer  
16, the EPS1 pin 25 of the connector 18 is floated. The EPS1  
25 pin 25 is connected to the base of the first transistor 28.  
When the EPS1 pin 25 is floated, the base of the first  
transistor 28 is in high voltage and the first transistor 28  
is open. The first transistor 28 is further connected to the  
base of the second transistor 30. When the first transistor  
30 28 is open, the base of the second transistor 30 is in low  
voltage and the second transistor 30 is therefore conducted,  
which results in the conducting state of the third transistor

32. The current from the power 29 can then pass through the third transistor 32. Consequently, the testing signal generated by the testing signal generator 22 can be transmitted to the displaying circuit 14 via the switch circuit 24. The detecting circuit 26 controls the transmission of the H-BLANK testing signal into the input port of the displaying circuit 14 by controlling the open or conducting state of the controlling port, i.e. the transistor 32, of the switch circuit 24.

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Please refer to Fig. 5. Fig. 5 is a flow chart 40 of simulating video signals to generate video images with the H-BLANK signal according to the present invention. The flow chart 40 comprises the following steps:

15 Step 42: Begin;

Step 44: The detecting circuit 26 detects if there are video signals transmitted from the computer 16, so as to judge if the monitor 10 has built connection with the computer 16? If connection is built, go to step 46, otherwise go to step 52;

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Step 46: The EPS1 pin 25 of the connector 18 is grounded;

Step 48: The transistor 32 of the switch circuit 24 is open;

Step 50: The H-BLANK signal is not able to be transmitted to the input port of the displaying circuit 14 via the transistor 32, go to step 62;

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Step 52: The EPS1 pin 25 of the connector 18 is floated;

Step 54: The transistor 32 of the switch circuit 24 is conducted;

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Step 56: The amplitude of the H-BLANK signal is properly adjusted;



Step 58: The adjusted H-BLANK signal is transmitted to the input port of the displaying circuit 14 via the transistor 32;

Step 60: A full-white picture is shown on the display panel 12;

Step 62: End.

Compared with the prior art monitor, the monitor 10 of the present invention comprises a self testing circuit 20 for testing if the monitor is in a good operation. The self testing circuit 20 performs self testing function by means of the EPS1 sub signal contained in the video signals (IBM VGA signals) and the H-BLANK signal generated by the testing signal generator 22. When a user wants to know if the monitor is in a good condition, he can disconnect the monitor from a computer. When the detecting circuit 26 detects no video signals transmitted from the computer 16, the H-BLANK testing signal would be transmitted to the displaying circuit 14 via the switch circuit 24. If a full-white picture is shown on the display panel 12 of the monitor 10, that means the monitor is in a good condition.

Therefore, the monitor according to the present invention can achieve the self testing function even lack of OSD facility or micro-controller.

Those skilled in the art will readily observe that numerous modifications and alterations of the device may be made while retaining the teachings of the invention. Accordingly, the above disclosure should be understood to be limited only by the bounds of the following claims.